

update Search 10/8/07, 6:92

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	56424	NAND near gate\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 08:55
L2	19	(NAND near gate\$1) and (input near terminal\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 08:56
L3	18263	(NAND near gate\$1) and (input near terminal\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 08:56
L4	14012	(NAND near gate\$1) and (input near terminal\$1) and (output near terminal\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 08:57
L5	194494	(("326") or ("327") or ("345")).CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/27 08:58
L6	3663	4 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 09:04
L7	144	6 and (NAND near logic near gate\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 09:28
L8	2024	6 and (NOR near gate\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 09:28
L9	1935	6 and (NOR near gate\$1) and invert\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 09:29
L10	1833	6 and (NOR near gate\$1) and inverter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 09:29

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L12	1098	6 and (NOR near gate\$1) and inverter and semiconductor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 09:31
L13	453	6 and (NOR near gate\$1) and inverter and (comparing or comparation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 09:32